L Number	Hits	Search Text	DB	Time stamp
7	2	("5867399").PN.	USPAT; US-PGPUB; EPO; JPO;	2003/10/06 08:55
8	2	("5623418").PN.	DERWENT USPAT; US-PGPUB; EPO; JPO;	2003/10/06 08:58
9	2	("5544067").PN.	DERWENT USPAT; US-PGPUB; EPO; JPO;	2003/10/06 08:58
10	2	("5220512").PN.	DERWENT USPAT; US-PGPUB; EPO; JPO;	2003/10/06 09:04
11	2	("5222030").PN.	DERWENT USPAT; US-PGPUB; EPO; JPO;	2003/10/06
-	2	("5867399").PN.	DERWENT USPAT; US-PGPUB; EPO; JPO;	2003/10/02 11:01
	341528	(IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)	DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:16
-	105	((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 15:21
-	2	or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)) (return near3 path near3 current) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:46
_	1147	or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) (current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI	USPAT; US-PGPUB;	2003/10/02 13:47
	1	or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)) (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)) and ((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same	EPO; JPO; DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:48
_	6080	(design\$3 or model\$4 or simulat\$3))) 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO;	2003/10/02 13:48
-	12	((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.	DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:51

Search History

10/6/03 10:11:17 AM

-	1	shield\$3 and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:52
		simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)))		
-	6	shield\$3 and (((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 13:59
_	38	simulat\$3))) and 716/\$.ccls.) (shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 14:09
	14	simulat\$3))) 716/\$.ccls. and ((shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 14:11
_	1	<pre>(return near1 path) and (716/\$.ccls. and ((shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and (schematic near2 (design\$3 or layout or model\$4 or simulat\$3)) and (physical near2 (design\$3 or layout or model\$4 or simulat\$3)) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3)))))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 14:11
-	6	(shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 14:13
-	1	((floor adj1 plan\$4) or floorplan\$4 or floor-plan\$4) and ((shield\$3 or defenc\$3 or protect\$3 or guard\$3) and (((current near3 return near3 path) and ((IC or (integrated adj1 circuit) or VLSI or LSI or circuit or ASIC or AMS) same (design\$3 or model\$4 or simulat\$3))) and 716/\$.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/03 08:43
-	73		USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/02 15:24

-	48	(ASIC or (application adj1 specific adj1	USPAT;	2003/10/02
		integrated adj1 circuit) or AMS or	US-PGPUB;	15:26
		(analog adj2 mixed adj1 signal)) and	EPO; JPO;	
		((((floor adj1 plan\$4) or floorplan\$4 or	DERWENT	
		floor-plan\$4) and (schematic near2		
		(design\$3 or layout or model\$4 or	ŀ	
		simulat\$3)) and (physical near2 (design\$3		
		or layout or model\$4 or simulat\$3)) and		
		((IC or (integrated adj1 circuit) or VLSI		
		or LSI or circuit or ASIC or AMS) same		
		(design\$3 or model\$4 or simulat\$3))) and		
		716/\$.ccls.)		
1_	19	parameterized and ((ASIC or (application	USPAT;	2003/10/02
		adjl specific adjl integrated adjl	US-PGPUB;	15:28
		circuit) or AMS or (analog adj2 mixed	EPO; JPO;	13.23
		adj1 signal)) and ((((floor adj1 plan\$4)	DERWENT	•
		or floorplan\$4 or floor-plan\$4) and	BBINIDIVI	
		(schematic near2 (design\$3 or layout or		
		model\$4 or simulat\$3)) and (physical		
l		near2 (design\$3 or layout or model\$4 or		
1		simulat\$3)) and ((IC or (integrated adj1	'	
1		circuit) or VLSI or LSI or circuit or		
		ASIC or AMS) same (design\$3 or model\$4 or	İ	
		, , , , , , , , , , , , , , , , , , , ,		
	,	simulat\$3))) and 716/\$.ccls.))	HGDDM.	2002/10/02
-	2	("6342823").PN.	USPAT;	2003/10/03
			US-PGPUB;	08:43
			EPO; JPO;	
			DERWENT	